

Abstract

A pulse modulation arrangement for a switch mode regulator includes a frequency controlled oscillator. A counter is enabled by a clock signal for the regulator to count
5 a controlled number of output pulses of the oscillator and thereby produce a pulse modulation signal. The oscillator frequency and count number are controlled by digital and optionally analog feed forward and/or feedback control signals of the regulator. The oscillator can comprise binary weighted
10 current sources and/or capacitors, or delay elements.